|  |
| --- |
| **Program/Discipline Learning Outcomes** |
| **Academic Year: 2018** |
| **Program/Discipline: (indicate program/discipline here) A.AS. Electronic Technology** |
| **PROGRAM/DISCIPLINE CONTACT** |
| **FACULTY: Edward Burkhead** | **OFFICE: JW 117** |
| **EMAIL: BurkheaEdwardL@jccmi.edu** | **PHONE:** |
| **Learning Outcome:****(Successful students will be able to)** | **Course(s) in which the assessment will be performed** | **Semesters the outcome will be assessed** | **Assessment tools used for learning outcome** |
| 1 | Analyze, troubleshoot and service computer systems | CIS 174, CIS 175, CIS 176 | W2018 | Examinations and Projects |
| 2 | Describe, analyze, and troubleshoot electrical circuits | ELT 120, ELT 126 | F2017, W2018 | Examinations and Labs |
| 3 | Describe electrical components and their effects on circuit behavior | ELT 126, ELT 130, ELT 140 | F2017, W2018 | Examinations and Labs |
| 4 | Design programmable circuits to achieve a desired outcome | ELT 140 | F2017, W2018 | Projects |
| 5 |  |  |  |  |